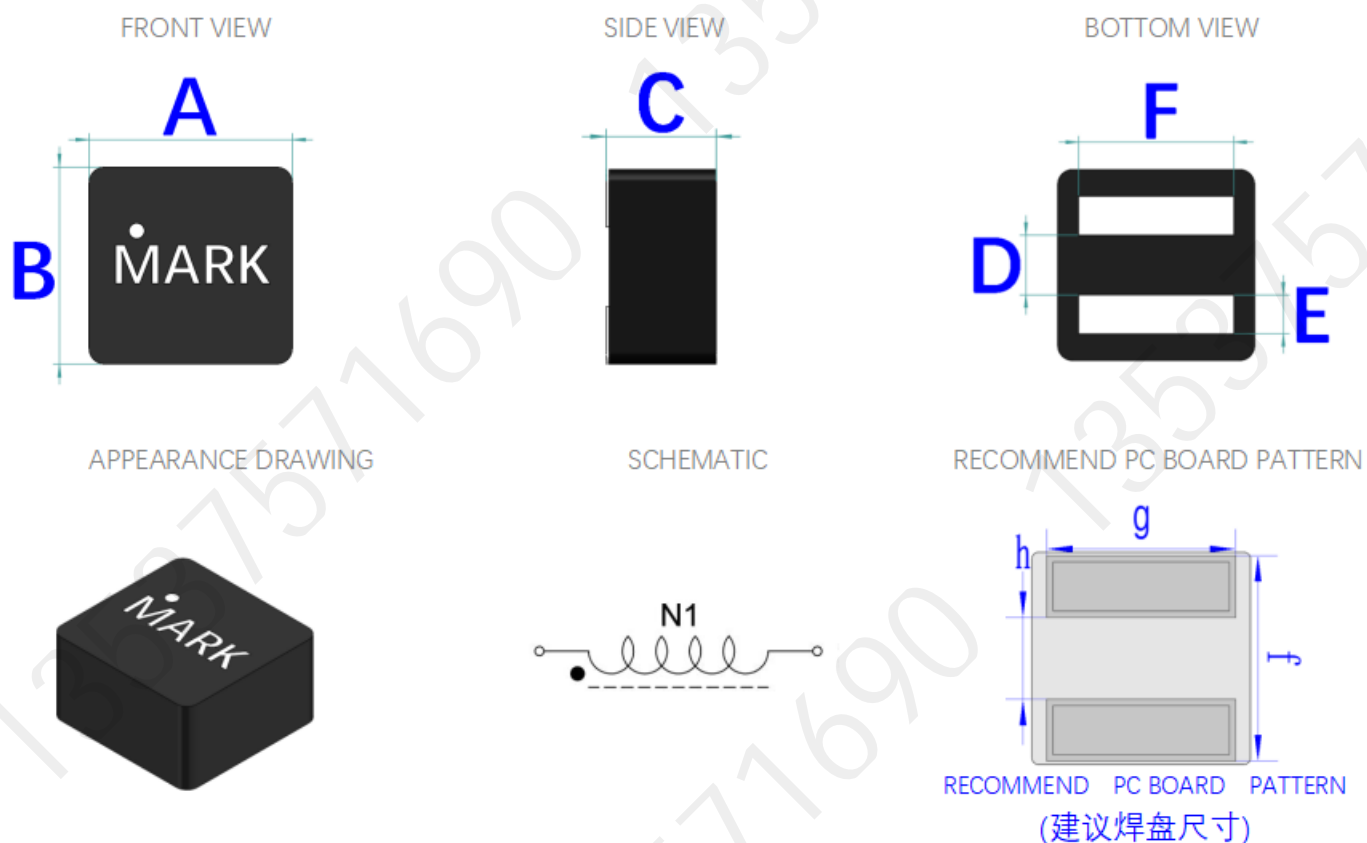


# Power Choke Coil



## ◆ Dimensions

Unit: mm ±0.30

Type	A	B	C	D	E	F	h	g	f
JSEL720D	7.80	7.80	1.80	2.80	2.40	7.80 TYP	2.5	8.2	8.0
JSEL725D	7.80	7.80	2.30	2.80	2.40	7.80 TYP	2.5	8.2	8.0
JSL773D	7.80	7.80	2.90	2.80	2.40	7.80 TYP	2.5	8.2	8.0
JSEL7D4SD	7.80	7.80	3.90	2.80	2.40	7.80 TYP	2.5	8.2	8.0
JSL775D	7.80	7.80	3.90	2.80	2.40	7.80 TYP	2.5	8.2	8.0
JSEL125	11.60	10.50	2.30	4.450	2.20	9.50 TYP	4.0	10.0	9.0
JSEL103D	11.60	10.50	2.80	4.450	2.20	9.50 TYP	4.0	10.0	9.0
JSEL140D	11.6	10.5	4.10 MAX	4.450	2.20	9.50 TYP	4.0	10.0	9.0
JSEL145D	11.6	10.5	4.60 MAX	4.450	2.20	9.50 TYP	4.0	10.0	9.0
JSEL106D	11.6	10.5	6.10 MAX	4.45	2.20	9.50Typ	4.0	10.0	9.0

## ◆ Electrical Characteristics

### JSEL720D Typ

Part No.	Inductance ( $\mu\text{H}$ )	Test Freq.	DCR (m $\Omega$ )		Isat(A)		Irms(A) 40°C rise
			TYP	MAX	MAX	TYP	MAX
JSEL720D-2R2M	2.20	1MHz/1.0V	10.10	12.80	14.5	16.5	12.0

### JSEL725D Typ

Part No.	Inductance ( $\mu\text{H}$ ) $\pm 20\%$	Test Freq.	DCR (m $\Omega$ )		Isat(A)		Irms(A) 40°C rise
			TYP	MAX	TYP	MAX	MAX
JSEL725D-2R2M	2.20	1MHz/1.0V	7.80	9.50	18.2	16.5	16.2

### JSL773D Typ

Part No.	Inductance ( $\mu\text{H}$ ) $\pm 20\%$	Test Freq.	DCR (m $\Omega$ )		Isat(A)		Irms(A) 40°C rise
			TYP	MAX	TYP	MAX	MAX
JSL773D-1R0M	1.00	1MHz/1.0V	3.50	3.80	28.0	25.0	24.7
JSL773D-1R5M	1.50	1MHz/1.0V	5.80	6.50	23.5	21.0	20.7
JSL773D-2R2M	2.20	1MHz/1.0V	8.10	9.00	18.0	16.5	16.2
JSL773D-3R3M	3.30	1MHz/1.0V	12.2	13.5	12.8	11.5	11.2
JSL773D-4R7M	4.70	1MHz/1.0V	15.8	17.5	10.5	9.50	9.20

### JSEL7D4SD Typ

Part No.	Inductance ( $\mu\text{H}$ )	Test Freq.	DCR (m $\Omega$ )		Isat(A)		Irms
			TYP	MAX	TYP	MAX	MAX
JSEL7D4SD-2R2MRP	2.2 $\pm 20\%$	1MHz/1.0V	5.5	6.6	24.0	20.0	20.0
JSEL7D4SD-3R3MRP	3.3 $\pm 20\%$	1MHz/1.0V	9.0	10.8	15.0	12.5	12.5
JSEL7D4SD-4R7MRP	4.7 $\pm 20\%$	1MHz/1.0V	12.0	14.4	12.6	10.5	10.5
JSEL7D4SD-100MTP	10 $\pm 20\%$	1MHz/1.0V	30.0	36.0	11.0	10.0	9.5

### JSL775D Typ

JSE Part No.	Inductance (μH)	Test Freq.	DCR (mΩ)		Isat(A)		Irms(A) 40°C rise
			TYP	MAX	TYP	MAX	MAX
JSL775D-2R2MRP	2.2±20%	1MHz/1.0V	5.50	6.10	25.0	23.0	20.0
JSL775D-4R7MTP	4.7±20%	1MHz/1.0V	9.20	11.20	19.0	18.0	14.5
JSL775D-100MTP	10±20%	1MHz/1.0V	20.50	23.50	16.00	13.8	12.0
JSL775D-220MTP	22±20%	1MHz/1.0V	50.50	56.50	7.2	6.5	6.0

### JSEL125 Typ

Part No.	Inductance (μH)	Test Freq.	DCR (mΩ)		Isat(A)		Irms(A) 40°C rise
			TYP	MAX	MAX	TYP	MAX
JSEL125-2R2MRP	2.2±20%	1MHz/1.0V	6.50	8.60	22.0	20.0	19.0
JSEL125-4R7MRP	4.7±20%	1MHz/1.0V	18.0	23.0	19.0	17.0	14.5

### JSEL103D Typ

Part No.	Inductance (μH)	Test Freq.	DCR (mΩ)		Isat(A)		Irms(A) 40°C rise
			TYP	MAX	MAX	TYP	MAX
JSEL103D-2R2MRP	2.2±20%	1MHz/1.0V	5.50	6.60	25.0	23.0	20.0
JSEL103D-4R7MRP	4.7±20%	1MHz/1.0V	9.20	11.20	19.0	18.0	14.5
JSEL103D-6R8MTC	6.8±20%	1MHz/1.0V	18.0	23.0	16.0	14.5	12.5
JSEL103D-7R2MTC	7.2±20%	1MHz/1.0V	30.50	36.50	13.50	12.0	9.5
JSEL103D-100MTC	10±20%	1MHz/1.0V	35.50	40.50	12.50	10.8	8.5

## JSEL140D Typ

Part No.	Inductance ( $\mu\text{H}$ ) $\pm 20\%$	Test Freq.	DCR (m $\Omega$ )		Isat(A)		Irms(A) 40°C rise
			TYP	MAX	MAX	TYP	MAX
JSEL140D-2R2M	2.20	1MHz/1.0V	4.0	4.5	27.5	25.0	24.0
JSEL140D-3R3M	3.30	1MHz/1.0V	4.30	4.80	25.5	23.0	22.0
JSEL140D-4R7M	4.70	1MHz/1.0V	10.5	12.0	20.0	16.0	14.2
JSEL140D-100M	10.0	1MHz/1.0V	18.0	20.0	18.0	14.0	12.5

## JSEL145D Typ

Part No.	Inductance ( $\mu\text{H}$ )	Test Freq.	DCR (m $\Omega$ )		Isat(A)		Irms(A) 40°C rise
			TYP	MAX	MAX	TYP	MAX
JSEL145D-4R7M	4.7	1MHz/1.0V	5.5	6.9	23.0	22.0	19.7
JSEL145D-6R8M	6.8	1MHz/1.0V	10.6	12.5	20.0	19.5	17.5
JSEL145D-100M	10.0	1MHz/1.0V	15.5	17.0	17.5	16.0	14.5

### Note:

Rated current: Isat or Irms, whichever is smaller

1: DCR test data is referenced to 25°C ambient; DCR at the middle of P2

2: Isat: Peak current for approximately 30% rolloff at +25 °C

3: Irms: DC current for an approximate temperature rise of 40 °C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed 125 °C under worst case operating conditions verified in the end application.

Value,  $\Delta T < 40^\circ\text{C}$